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## \*BIBDATASHEET\*

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### APPLICANTS

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### \*\* CONTINUING DATA \*\*\*\*\*

*NONE - Dm*

### \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 99-240893 08/27/1999

### IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/06/2000

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>Dm</i> Examiner's Signature	JAPAN	2	1	1

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### TITLE

Signal combining circuit having two A/D converters

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